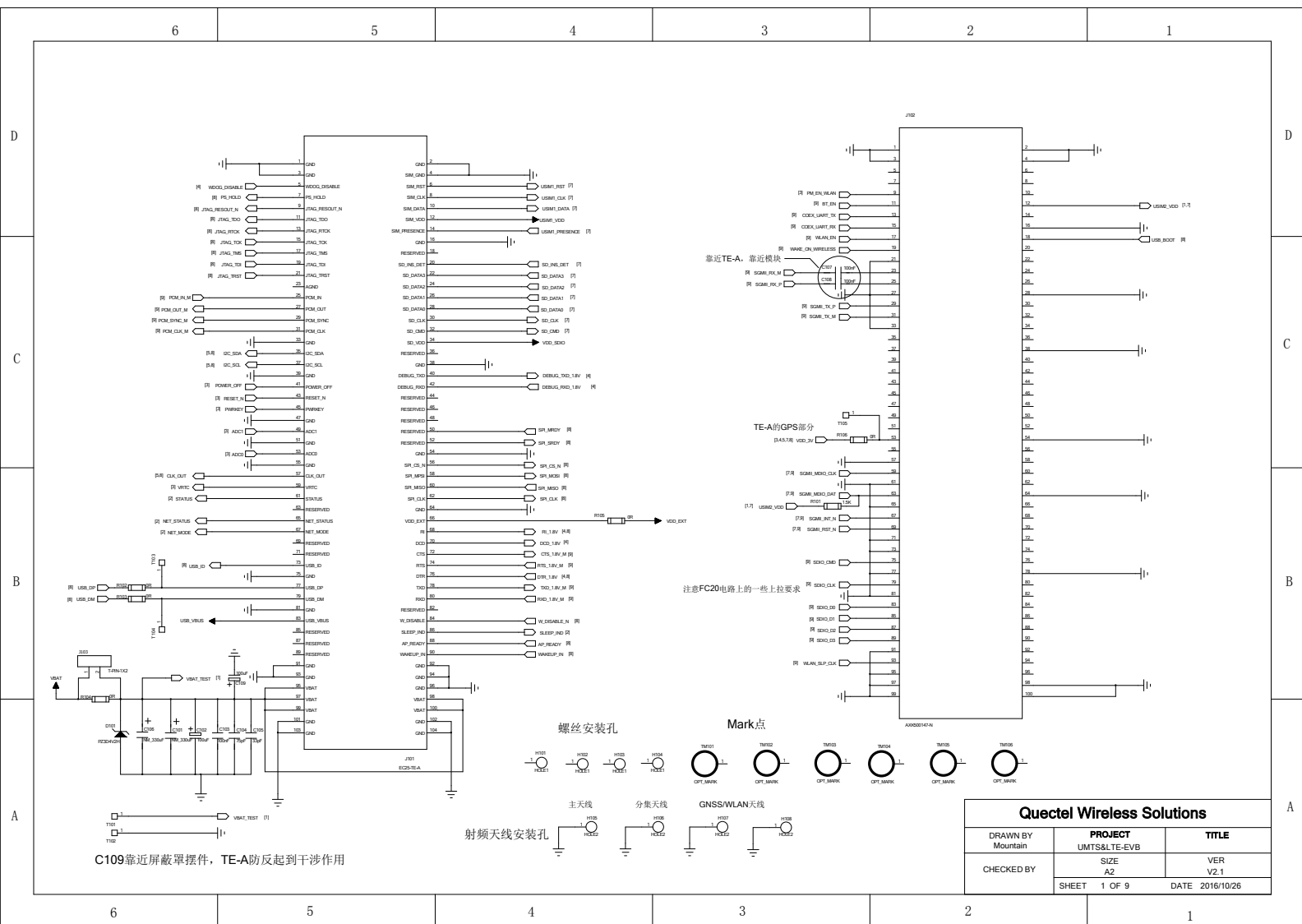
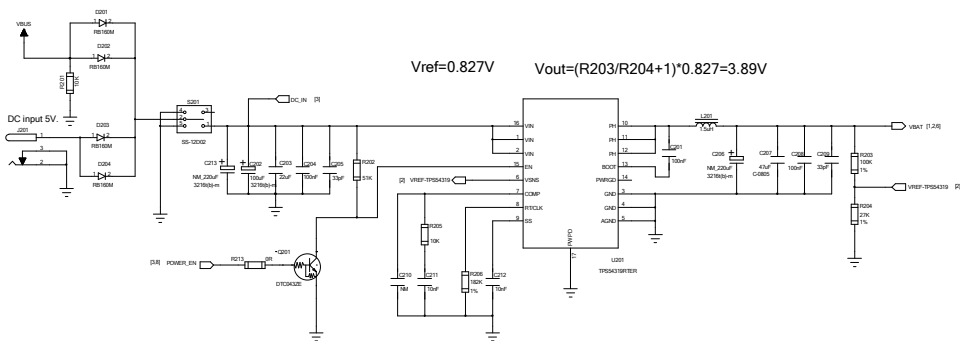
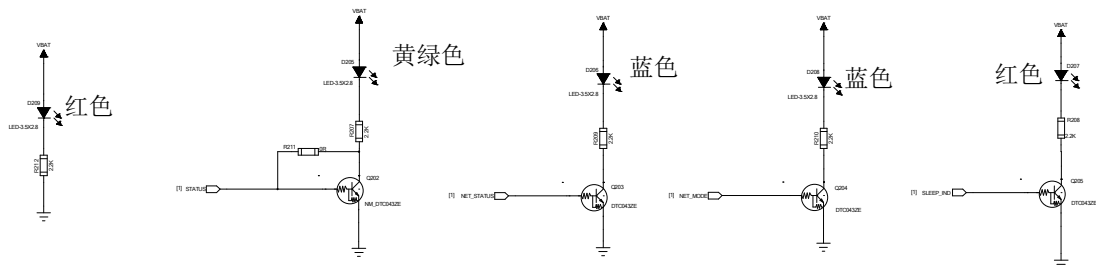




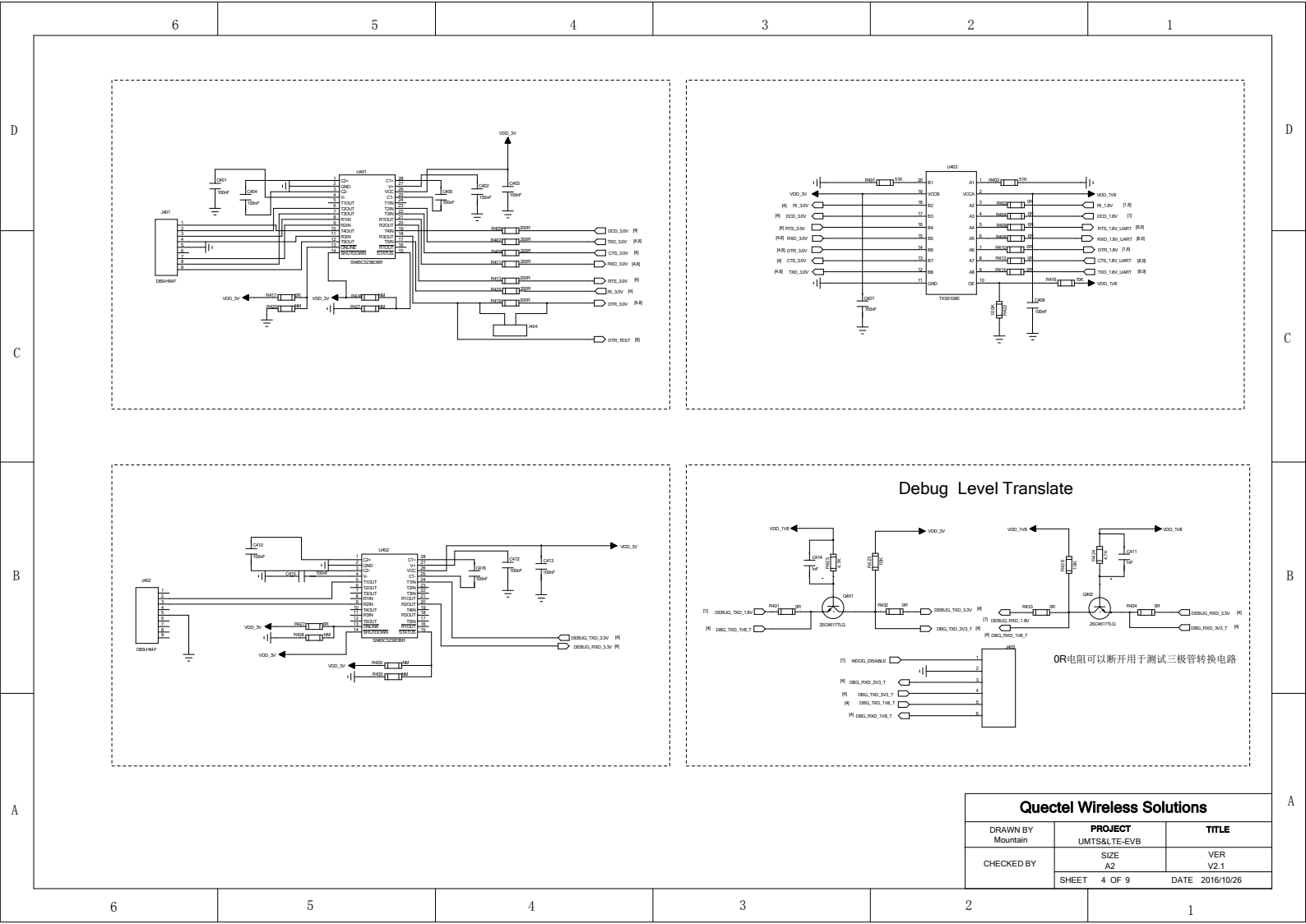


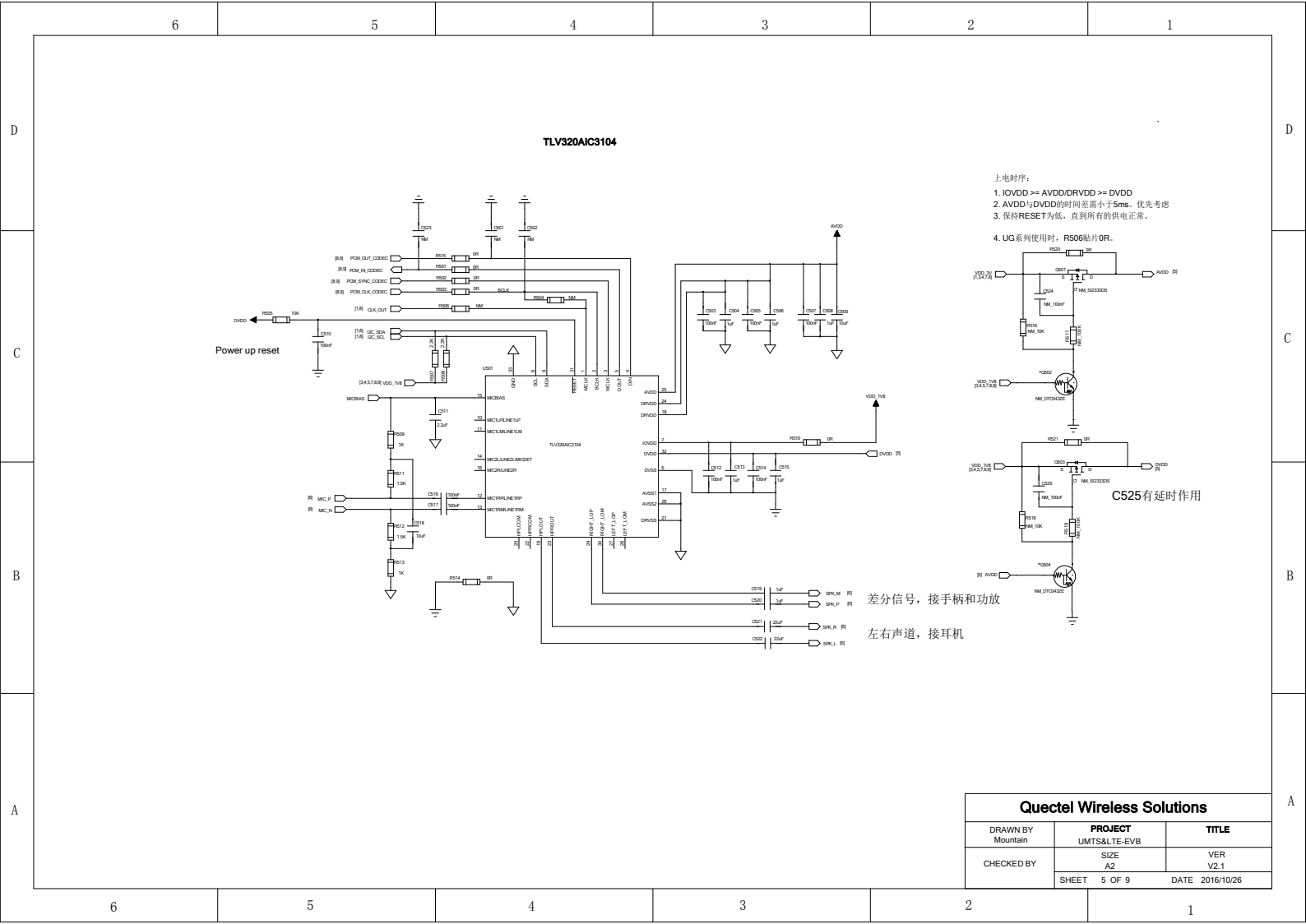
1. C201要求: 0.1uF, X7R/X5R, 10V or higher.
2. RT/CLK配置开关频率, 越高, 可以减小输出电容和电感, 但是会造成更高的开关损耗, 推荐1MHz, 对应R206=182K.
3. 电感MDMK4040T1R5MM / MDWK4040T1R5MM 前者额定电流4.5A, 后者可达7A, 但是封装高度增加到2mm了。
4. 电容的ESR需小于55mohm. 因此47uF电容选型JMK212BBJ476MG, 1MHz下小于0.01mohm 220uF选型T520B227M006ATE035, 35mohm, 暂不贴。
5. R205/C211为初步计算值, 待调试
6. 输入端的大电容同样应该选择low ESR的器件。

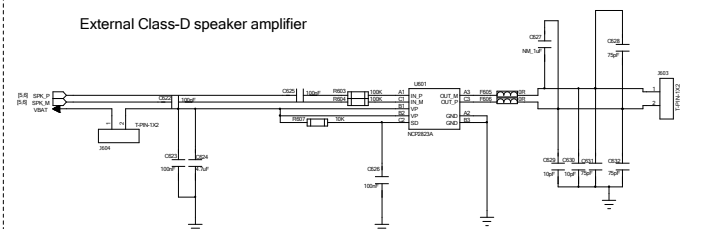
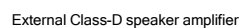
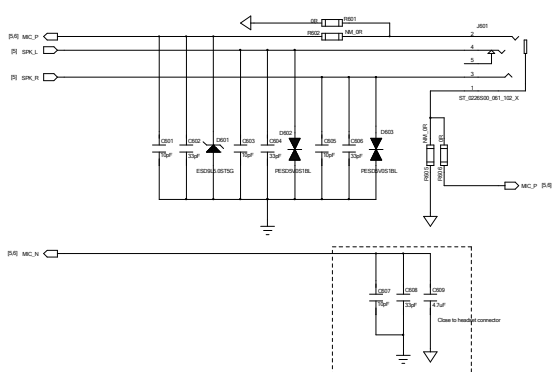


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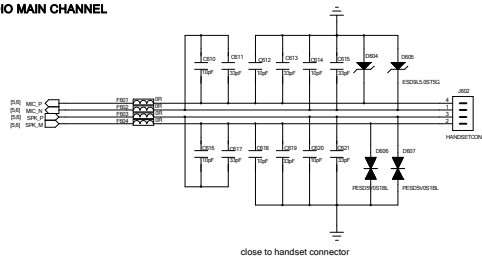
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CHECKED BY	SIZE A2	VER V2.1
SHEET 2 OF 9	DATE 2016/10/26	





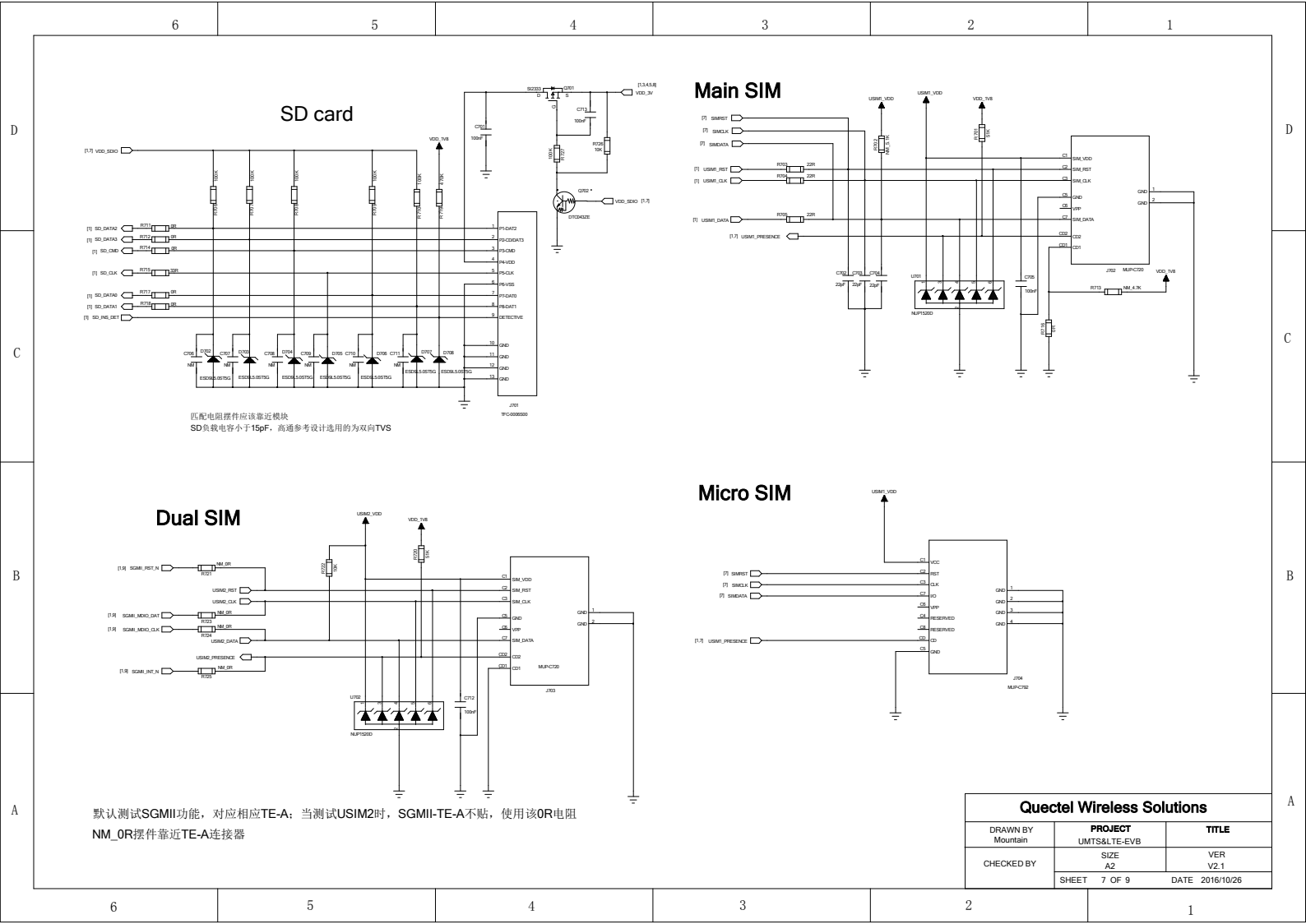


AUDIO MAIN CHANNEL

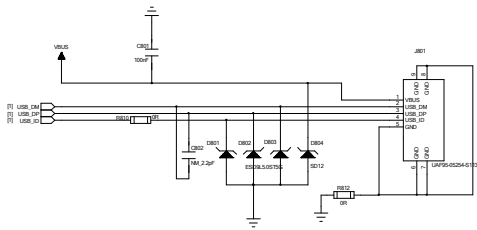


	CTIA (美标)	OMTP
R601/R606	NM	M
R602/R605	M	NM

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CHECKED BY	SIZE A2		VER V2.1
	SHEET 6 OF 9	DATE 2016/10/26	

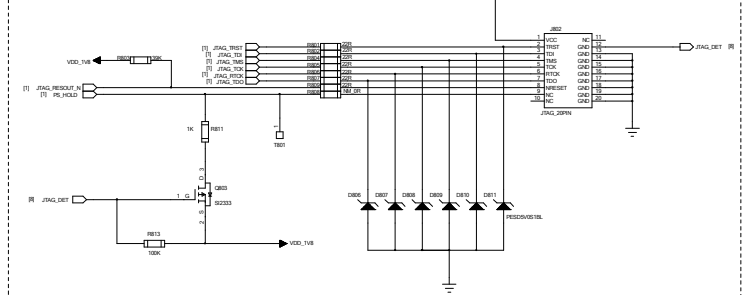


USB

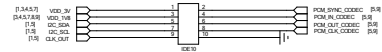


USB线路采用双向ESD

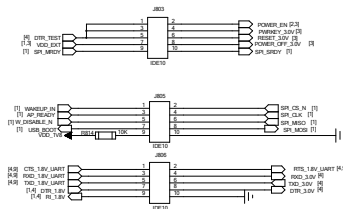
JTAG



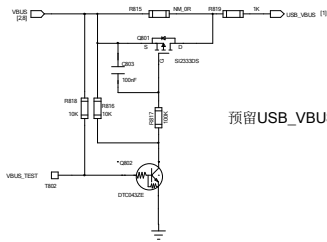
MAX9860使用



Testpoints



预留USB_VBUS开关控制电路



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SHEET	8 OF 9	DATE 2016/10/26

